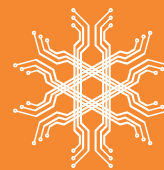


Conference at a Glance

Monday, April 16

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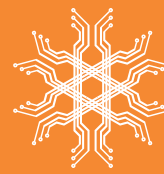
	General Interest	ISA Developer Days	GCC Improvements	Virtualization	Research/Advanced Topics	
8:00	Registration <i>Bayshore Foyer</i> & Exhibits Open <i>Sierra & Cascade</i>					8:00
9:00	Opening Session, Mark K. Smith, Gelato Central Operations <i>Siskiyou & Donner Pass</i>					9:00
10:00	Keynote —Enterprise Workloads on Itanium-Based Servers Jean Bozman, IDC <i>Siskiyou & Donner Pass</i>					10:00
	Gelato Special Announcement <i>Siskiyou & Donner Pass</i>					
11:00	LRZ's Recent Altix 4700 Installation Iris Christadler, LRZ <i>Siskiyou</i>	Basic Intel Itanium Architecture Cameron McNairy & Eric W. Moore, Intel <i>Donner Pass</i>	Introduction <i>Fir</i> ISP-RAS Projects on Improving GCC for Itanium Arutyun Avetisyan & Andrey Belevantsev, ISP-RAS <i>Fir</i>	Introduction <i>Oak</i> Kernel-Based Virtual Machine (KVM) for IPF Fenghua Yu, Intel <i>Oak</i>		11:00
12:00	S7 Case Study Pankaj Kulkarni, S7 Solutions <i>Siskiyou</i>		CERN Snippets Dissected Jose Dana, CERN <i>Fir</i>	Virtualizing the Performance Monitoring Unit Stéphane Eranian, HP <i>Oak</i>		12:00
13:00	Lunch & Exhibits Open <i>Sierra & Cascade</i>	Gelato Member Luncheon <i>San Jose</i>				13:00
		64-Bit Migration to Linux on Itanium Architecture Jonathan Ward, HP <i>Donner Pass</i>				
14:00		Optimizing Software with Intel Compilers Eric W. Moore, Intel <i>Donner Pass</i>	Update on the Gelato GCC Build Farm Matthieu Delahaye, Gelato Central Operations <i>Fir</i>	Xen/ia64 Progress and Status Joseph Szczeppek, HP <i>Oak</i>	uProfiler: A Concurrent Profiler for Concurrent C++ Peter Buhr & Justyna Gidzinski, Waterloo <i>Siskiyou</i>	14:00
15:00		Locating Optimization Opportunities with VTune Gary Carleton, Intel <i>Donner Pass</i>	Update on Prefetching Work Zdenek Dvorak, SUSE <i>Fir</i>	Porting Xen to the Altix Jes Sorensen, SGI <i>Oak</i>	Determining Chebyshev and Remez Expansions Clemens Roothaan, Honorary Member <i>Siskiyou</i>	15:00
16:00		Reading and Interpreting Stall Counters Murali Vijayasundaram & Sujoy Saraswati, HP <i>Donner Pass</i>	Update on Alias Analysis Work Diego Alejandro Novillo, Redhat/GCC <i>Fir</i>	LinuxOnLinux: A UML Work-Alike for IA-64 Peter Chubb, UNSW <i>Oak</i>		16:00
	Poster Session #1 / Afternoon Break / Exhibits Open <i>Sierra & Cascade</i>					
17:00	Spotlight —The Significance of Multi-Core: The Intel Perspective, Xinmin Tian, Intel <i>Siskiyou & Donner Pass</i>					17:00
	Spotlight —Multi-Core Programming & Research: The Future of Itanium, Wen-mei Hwu, UIUC <i>Siskiyou & Donner Pass</i>					
18:00	Exhibits Open <i>Sierra & Cascade</i>					18:00
19:00						19:00
20:00						20:00
21:00						21:00



Tuesday, April 17

Tuesday, April 17

General Interest	Multi-Core Programming	Tools & Tuning	GCC Improvements	Virtualization	IA-64 Linux Kernel	Research/Advanced Topic
8:00						
Registration Bayshore Foyer & Exhibits Open Sierra & Cascade						
9:00						
Spotlight —Itanium Processor Vision and Roadmap James D. Fister, Intel Siskiyou & Donner Pass						
10:00						
ISP-RAS Activities in Open-Source Software Victor Ivannikov, ISP-RAS Siskiyou		HPCPI/Xtools Performance Analysis Toolset David C.P. LaFrance-Linden, HP Donner Pass	Update on Superblock Work Robert Kidd, UIUC Fir	An Overview of OpenVZ Virtualization Technology Kir Kolyshkin, OpenVZ Oak		
11:00	Different Approaches to Code in Itanium Assembly Sverre Jarp, CERN Siskiyou	OpenMP Applications with Intel Cluster OpenMP Lawrence F. Meadows, Intel Donner Pass	GCC and Osprey Update Shin-Ming Liu, HP Fir	HP Integrity Virtual Machines Technology Overview Todd Kjos, HP Oak		11:00
12:00	CPU2006 on IPF Gerolf F. Hoflehner, Intel Siskiyou		Compiling Debian Using GCC 4.2 and Osprey Martin Michlmayr, Debian Fir	BladeSymphony with Virtage Paul Figliozzi, Hitachi Oak		12:00
Lunch & Exhibits Open Sierra & Cascade						
13:00						
14:00						
		Detect/Solve App. Mem. Problems Tom Archambault, Etnus Donner Pass	Interprocedural Optimization Framework Jan Hubicka, SUSE Fir		Deploying Linux/IA-64 in the Telecom Market Khalid Aziz, HP Oak	Discussion: Synchronization and Mem. Ordering Cameron McNairy, Intel Siskiyou
15:00						
		Caliper: HP's Performance Tool Murali Vijayasundaram & Stephen Williams, HP Donner Pass	Update on LTO Kenneth Zadeck, NaturalBridge, Inc. Fir		Machine Check Architecture Russ Anderson, SGI Oak	Work in Progress: NFS Research at UNSW Peter Chubb & Shehjar Tikoo, UNSW Siskiyou
16:00						
	Hyper-Threading on Dual-Core Itanium 2, Rohit Bhatia, Intel Fir	Optimized Itanium Binaries Using GCC and Tools Diego Alejandro Novillo, Redhat/GCC Donner Pass			On-The-Fly TLB Generation Christoph Lameter, SGI Oak	Experiences with High-Speed Linear Solvers Hugo Daniel Scolnik, UBA Siskiyou
Poster Session #2 / Afternoon Break / Exhibits Open Sierra & Cascade						
17:00						
Keynote —Linux Kernel Development for the Itanium Architecture Andrew Morton, Linux 2.6 Kernel Maintainer Siskiyou & Donner Pass						
Exhibits Open Sierra & Cascade						
18:00						
19:00						
Hawaiian Luau at the Doubletree Pine, Fir & Oak						
20:00						
21:00						



Wednesday, April 18

Wednesday, April 18

	Topics for Enterprise	Multi-Core Programming	Tools & Tuning	IA-64 Linux Kernel	Research/Advanced Topics
8:00	Registration Bayshore Foyer & Exhibits Open Sierra & Cascade				
9:00	Keynote —Itanium, A Popular Platform for Large Systems Wim Coekaerts, Oracle <i>Siskiyou & Donner Pass</i>				
10:00	Looking Ahead to Gelato ICE Singapore 2007, Jon Lau, NGO <i>Siskiyou & Donner Pass</i>				
		Parallel Programming Concepts Gary Carleton, Intel <i>Oak</i>		Future Challenges to Linux Scalability Andrew Morton, Linux 2.6 Kernel Maintainer <i>Pine / Fir</i>	
11:00	Solaris/SPARC Applications on Linux/Itanium Ian Robinson, Transitive <i>Donner Pass</i>	OpenMP Eric W. Moore, Intel <i>Oak</i>		Understanding Linux NUMA Memory Policies Lee Schermerhorn, HP <i>Pine / Fir</i>	Development of Modulo Scheduled Code John H. Detrich, Independent Contractor <i>Siskiyou</i>
12:00	Genuinely Secure Systems Bill S. Worley, Secure64 <i>Donner Pass</i>			NUMA Hardware Profile-Guided Page Placement Jaydeep Marathe, NCSU <i>Pine / Fir</i>	[IP]²
13:00	Lunch & Exhibits Open <i>Sierra & Cascade</i>				
14:00		Intel Thread Checker Gary Carleton, Intel & Matthieu Delahaye, Gelato <i>Oak</i>		Kexec/Kdump on IPF Nanhai Zou, Intel <i>Fir</i>	Lessons Learned from Research at BMD Jaesik Kwak, BMD <i>Siskiyou</i>
	The Itanium PAL (Processor Abstraction Layer) Tony Luck, Intel <i>Donner Pass</i>			Process Scheduling on 1024 Processors Christoph Lameter, SGI <i>Fir</i>	OpenUH: Exploring Language Constructs Barbara Chapman, Houston <i>Siskiyou</i>
15:00	Intel's Commitment to Linux Sunil Saxena, Intel <i>Donner Pass</i>	Intel Thread Profiler Gary Carleton, Intel & Matthieu Delahaye, Gelato <i>Oak</i>	Parallel Programming Tools Alexander Moskovsky, PSI-RAS <i>Siskiyou</i>	Distribution Panel: Redhat, Debian & openSUSE Prarit Bhargava, Redhat Dann Frazier, HP Nathan Conger, Novell <i>Fir</i>	
16:00			Update on OpenSpeedShop Martin Schulz, LLNL <i>Siskiyou</i>		Itanium 2 and Montecito Microarchitecture Cameron McNairy, Intel <i>Donner Pass</i>
	Afternoon Break				
17:00	Spotlight —Setting Up a Quiet Itanium-Based Home Office for Fun and Profit, David Mosberger, Gelato Honorary Member <i>Siskiyou & Donner Pass</i>				
18:00					
19:00					
20:00					
21:00					