



Conference at a Glance

Monday, October 2

	General Interest	ISA Software Developer	Grid
8:00	Registration & Exhibits Open <i>4th Floor Foyer</i>		
9:00	Welcome <i>Exploration</i>		
10:00	Keynote—Gelato: A Call to Arms, Steve Geary, HP <i>Exploration</i>		
	Morning Break <i>4th Floor Foyer</i>		
11:00	Update on LSB and Open Standards <i>Exploration</i>	Basic Intel Itanium Architecture <i>Breakthrough</i>	Grid Computing at CERN <i>Creation</i>
12:00	High-Performance Storage Solutions <i>Exploration</i>		A Security Monitoring System for the Grid <i>Creation</i>
13:00	Lunch <i>4th Floor Foyer</i>		
14:00	Compiler Design Criteria for Modulo Scheduled Itanium Codes <i>Exploration</i>		
15:00	Technical and Scientific Computing Performance <i>Exploration</i>	64-Bit Migration to Linux on Itanium <i>Breakthrough</i>	Itanium Projects: From R&D to Industry <i>Creation</i>
16:00	Poster Session / Afternoon Break <i>4th Floor Foyer</i>		
17:00			
18:00			
19:00			
20:00			
21:00			

Tuesday, October 3

	General Interest	ISA Software Developer	Scalability	Grid	Research / Advanced
8:00	Registration & Exhibits Open <i>4th Floor Foyer</i>				
9:00	Keynote—What Intel Itanium Architecture and Processors Can do for You, Cameron McNairy, Intel <i>Exploration</i>				
10:00	Morning Break <i>4th Floor Foyer</i>				
	Host Presentation—National Grid in Singapore Host Presentation—HPC and Grid R&D at IHPC				
11:00	<i>Exploration</i>				
	Next-Generation Bioinformatics Software <i>Exploration</i>	Optimizing Software with Intel Compilers <i>Breakthrough</i>			GPT and Superpages <i>Creation</i>
12:00	Lunch <i>4th Floor Foyer</i>				
13:00		Locating Optimization Opportunities w/ VTune <i>Breakthrough</i>	Scaling Linux to 1024 Processors <i>Exploration</i>		ClustalW Optimization: Adaptive Scheduling <i>Creation</i>
14:00		Performance Monitoring on Itanium <i>Breakthrough</i>	Open Session and Discussion <i>Exploration</i>		Compiling the Linux Kernel with icc <i>Creation</i>
	Afternoon Break <i>4th Floor Foyer</i>				
15:00		Update on the Perfmon2 Interface <i>Breakthrough</i>	HP/OSLO Linux Scalability Tracking and Investigations <i>Exploration</i>	Experiences with Itanium Clusters <i>Creation</i>	
16:00		Hyper-Threading on Dual-Core Itanium 2 <i>Breakthrough</i>		Open Session and Discussion <i>Creation</i>	
17:00					
18:00					
	Seafood Dinner <i>Palm Beach Seafood Restaurant</i>				
19:00					
20:00					
21:00					



Wednesday, October 4

	Multi-Core Programming	Virtualization	Scalability	Research / Advanced
8:00				
	Registration & Exhibits Open 4th Floor Foyer			
9:00	Keynote—Jeff Adie, SGI and Itanium Solutions Alliance <i>Exploration</i>			
10:00	Morning Break 4th Floor Foyer			
11:00	Multi-Core Programming Seminar and Lab <i>Breakthrough</i>		Open Session and Discussion <i>Exploration</i>	Particle Simulation Approach <i>Creation</i>
		Xen and Intel Virtualization Technology <i>Exploration</i>		Update on the Osprey Project <i>Creation</i>
12:00	Lunch 4th Floor Foyer			
13:00	Multi-Core (continued) <i>Breakthrough</i>	Evaluating Xen IA-64 <i>Exploration</i>		ISP RAS Activities for Improving GCC <i>Creation</i>
14:00		Virtualization and User-Level Drivers <i>Exploration</i>		Storage Layout Optimizations for PFS <i>Creation</i>
15:00		TBA <i>Exploration</i>		
	Afternoon Break 4th Floor Foyer			
16:00	Multi-Core (continued) <i>Breakthrough</i>			
17:00				
18:00				
19:00				
20:00				
21:00				